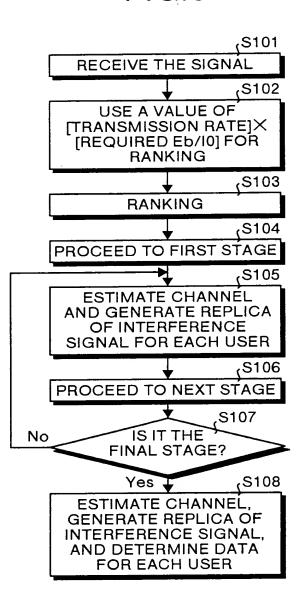
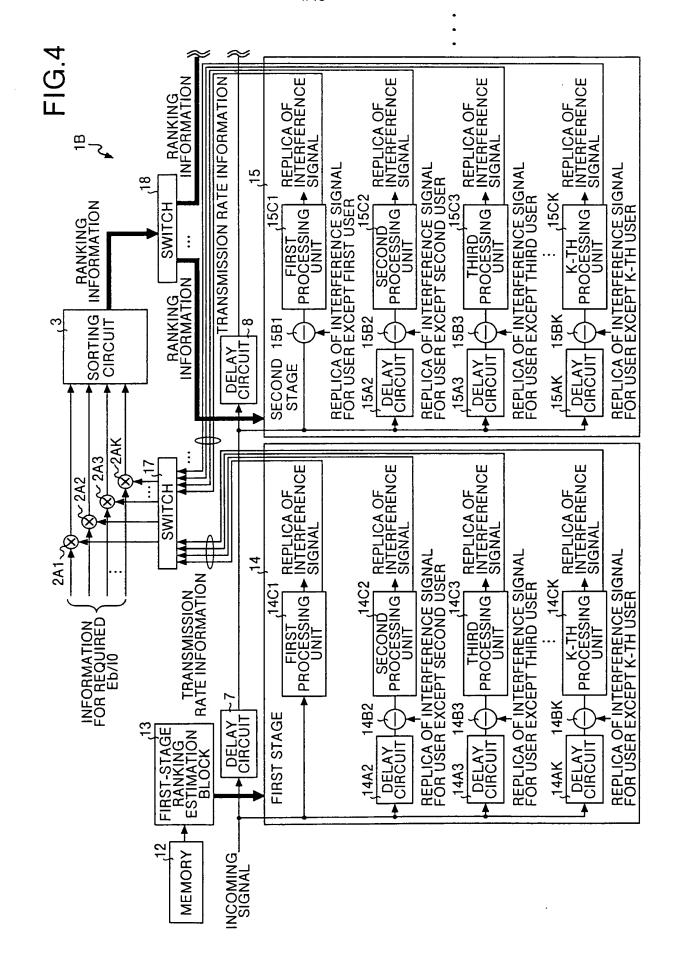
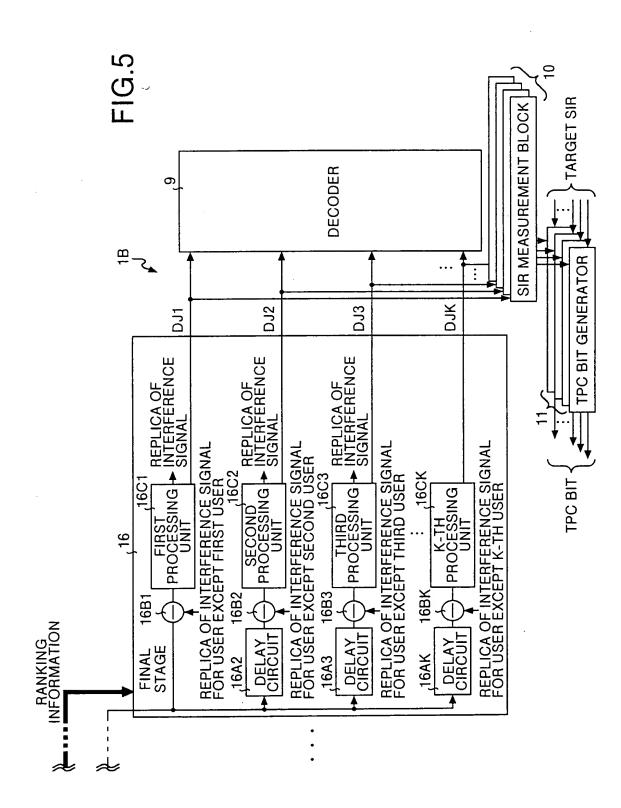


FIG.3









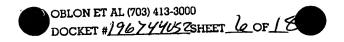
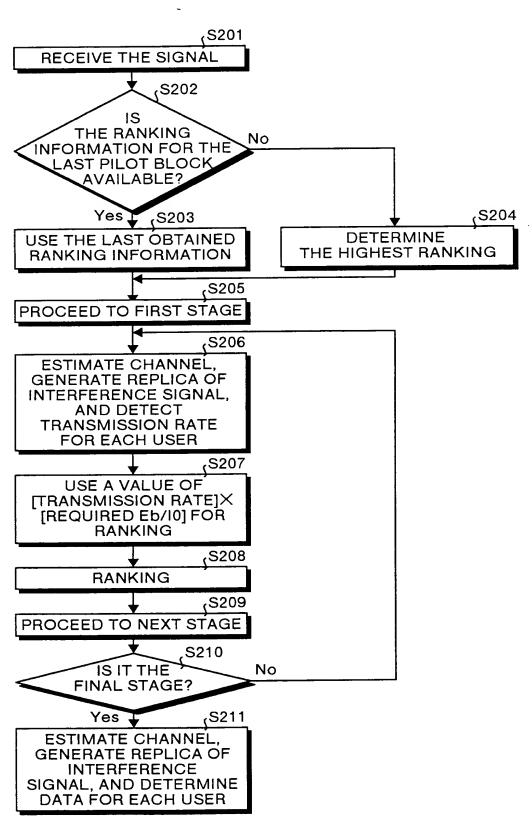
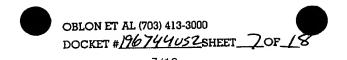
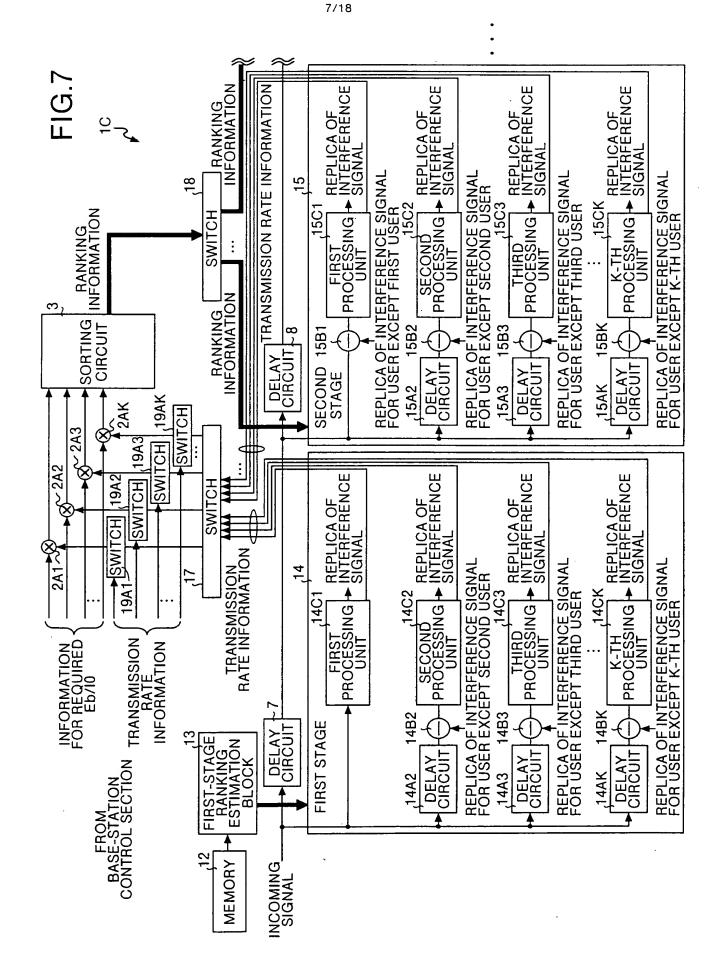
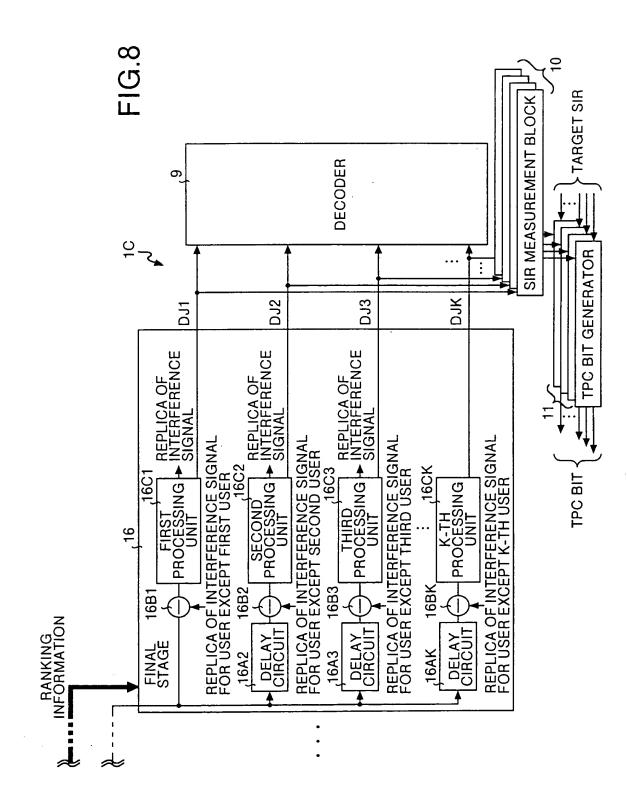


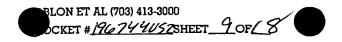
FIG.6

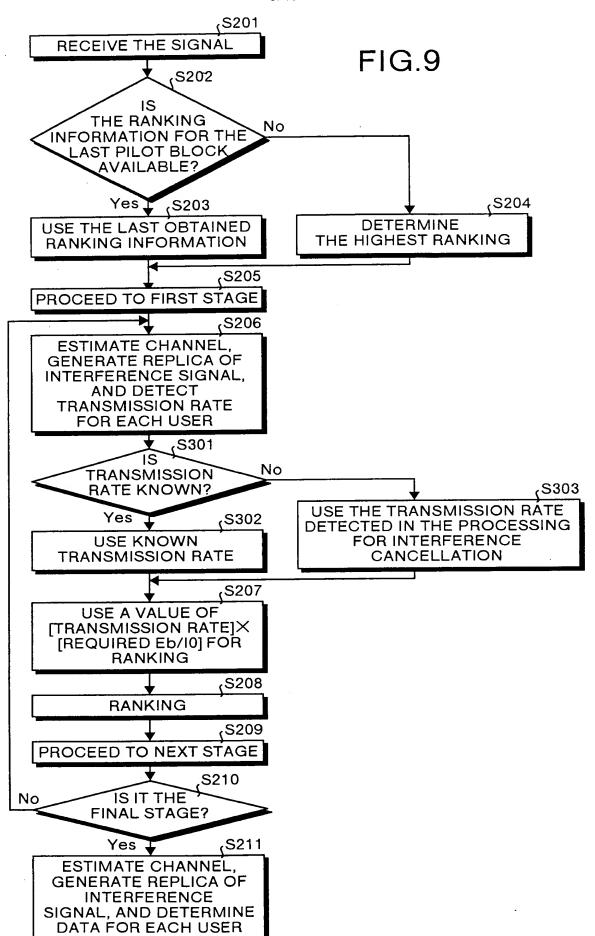


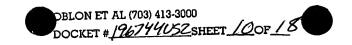


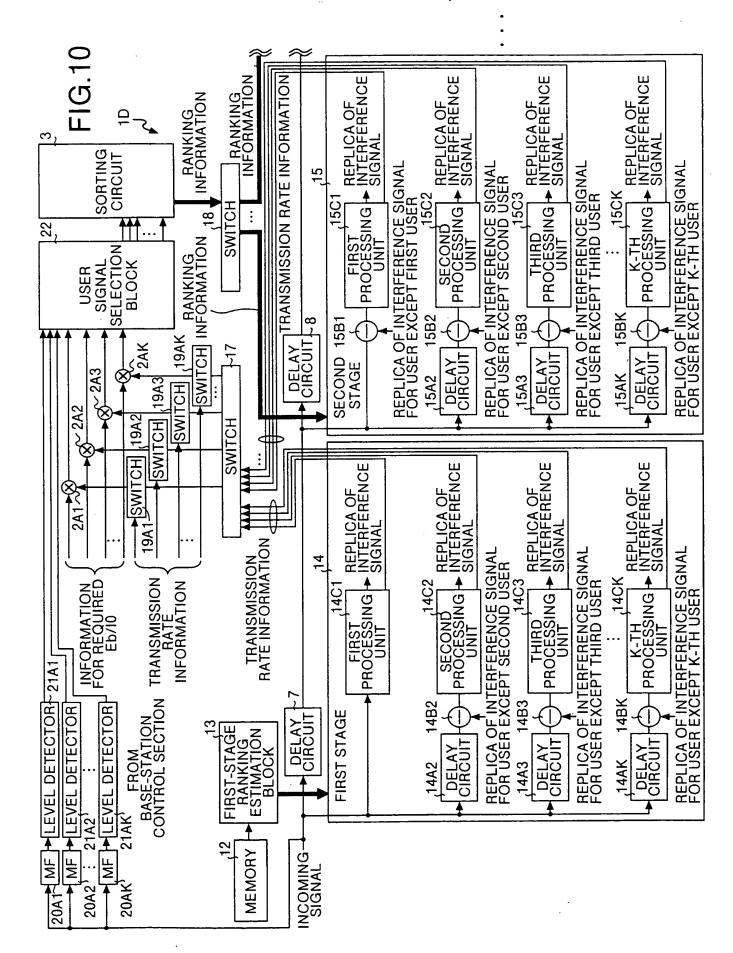


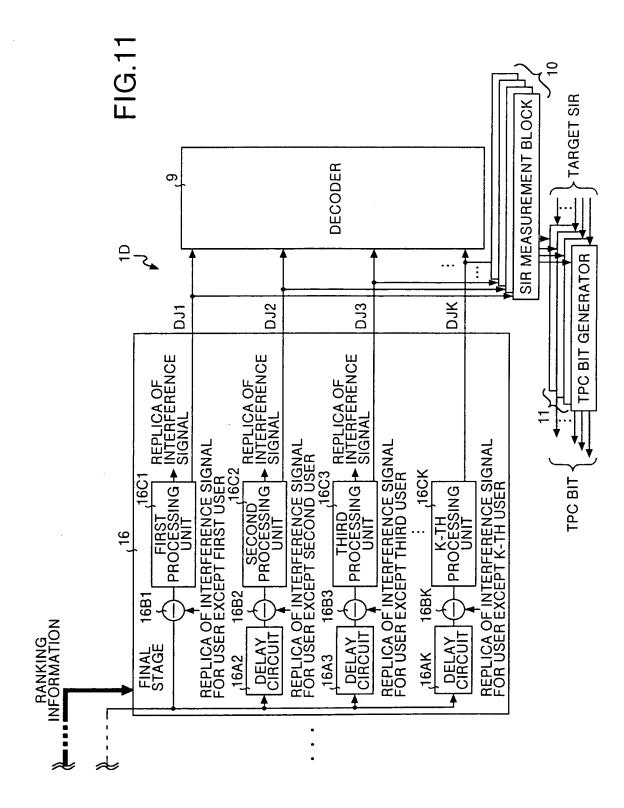


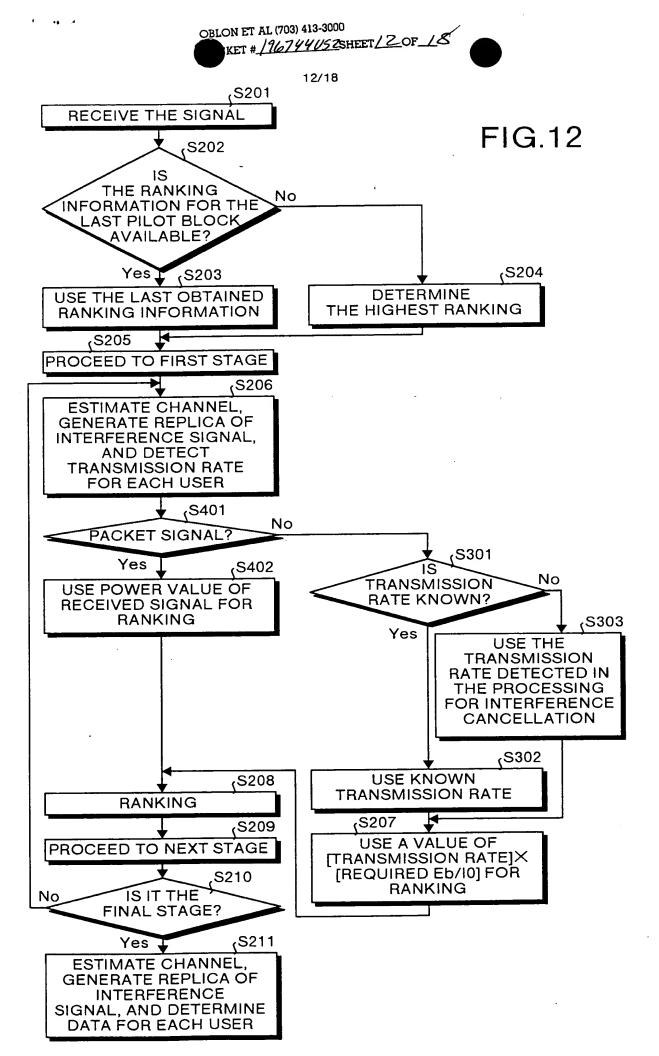


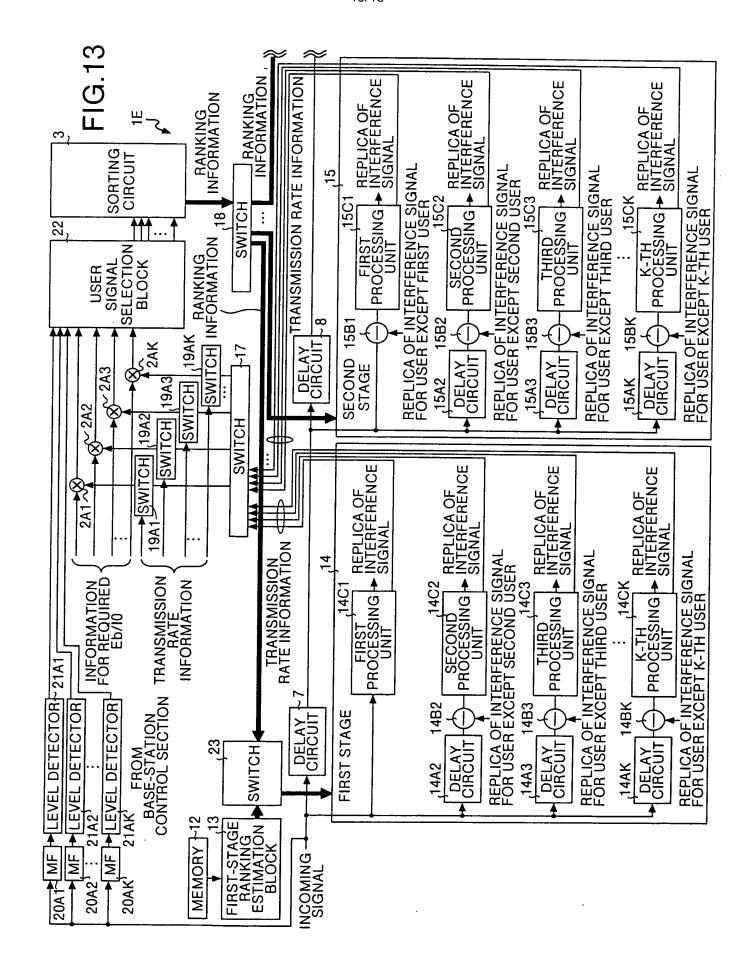




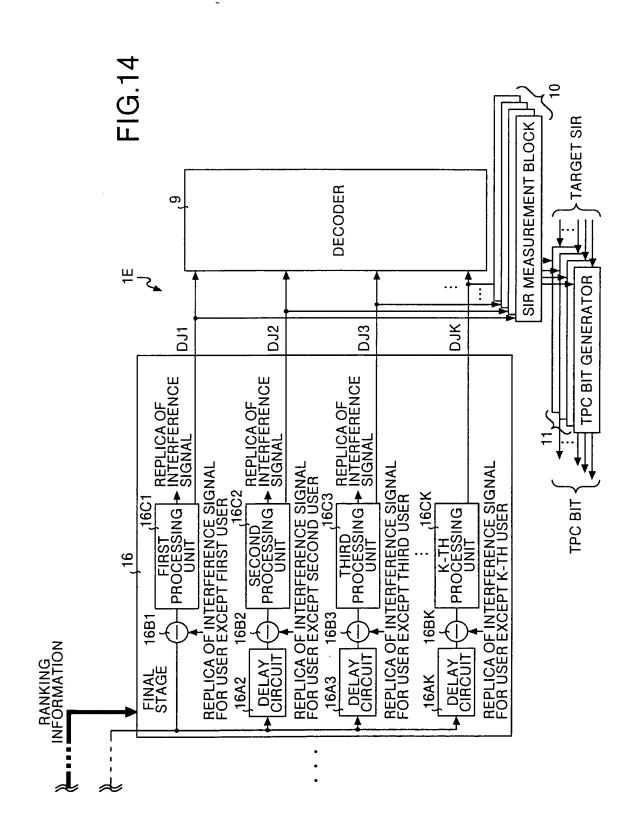






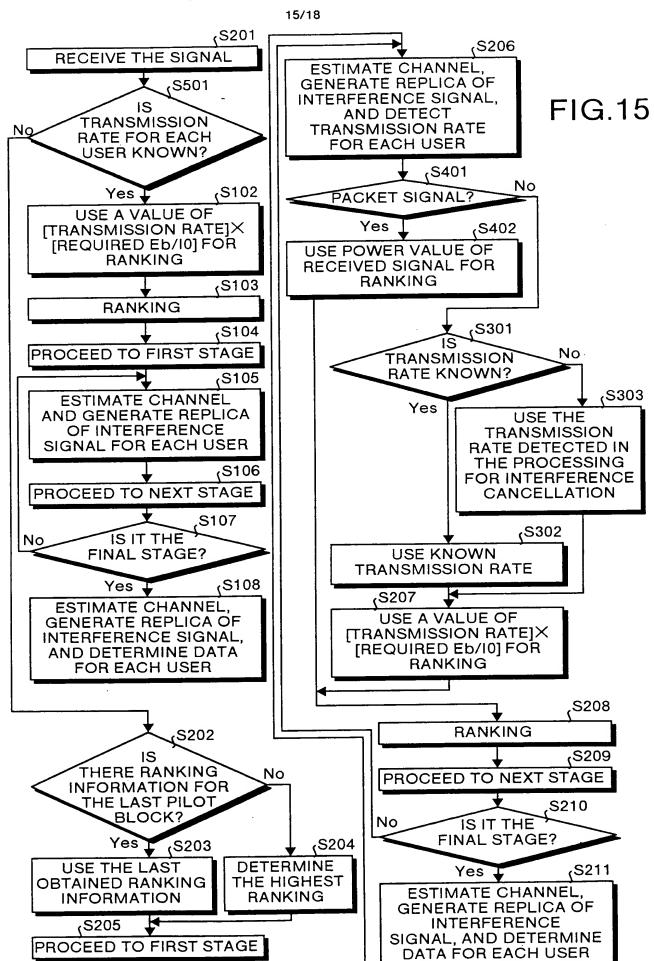


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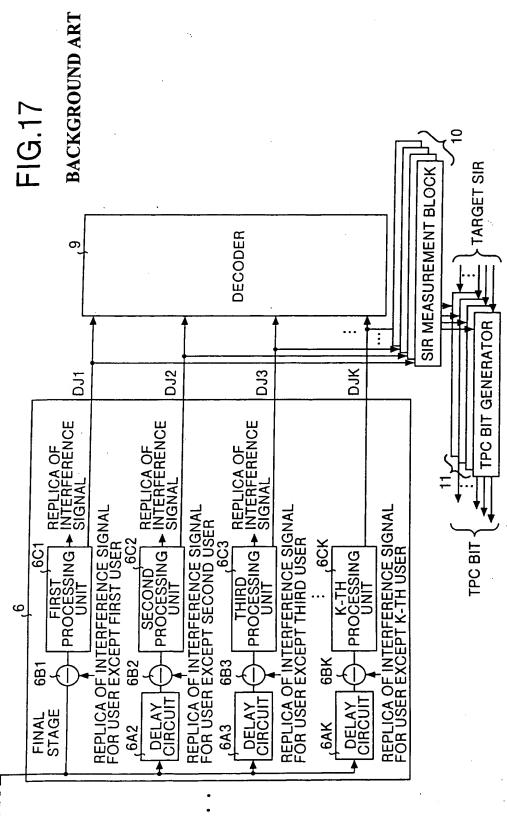
t No: 196744US-2 Instantor: Takeo SUZUKI, et al. Serial No: 09/650,084 Reply to OA dated: February 25, 2004 Replacement Sheet 16/18 FIG. 16 BACKGROUND ART MAY 1 9 2004 PRAJE TRADEMANT REPLICA OF INTERFERENCE SIGNAL REPLICA OF INTERFERENCE SIGNAL PROCESSING ► INTERFERENCE UNIT PROCESSING PINTERFERENCE SIGNAL REPLICA OF INTERFERENCE SIGNAL FOR USER EXCEPT FIRST USER 5A2 REPLICA OF INTERFERENCE SIGNAL FOR USER EXCEPT SECOND USER REPLICA OF INTERFERENCE SIGNAL FOR USER EXCEPT THIRD USER REPLICA OF INTERFERENCE SIGNAI FOR USER EXCEPT K-TH USER 5 A PROCESSING POUNT - PROCESSING -SCK $5C_1$ **58K 5B3 5B1** ω DELAY' CIRCUIT OFLAY DELAY DELAY SECOND STAGE **5A3** 5AK က SORTING REPLICA OF INTERFERENCE SIGNAL REPLICA OF INTERFERENCE SIGNAL REPLICA OF INTERFERENCE SIGNAL CIRCUIT REPLICA OF
INTERFERENCE
SIGNAL RANKING INFORMATION (21AK REPLICA OF INTERFERENCE SIGNAL FOR USER EXCEPT SECOND USER REPLICA OF INTERFERENCE SIGNAI FOR USER EXCEPT THIRD USER REPLICA OF INTERFERENCE SIGNAI FOR USER EXCEPT K-TH USER DETECTOR DETECTOR DETECTOR FIRST PROCESSING PUNIT 4CK R-TH / PROCESSING SECOND PROCESSING H UNIT PROCESSING HOUSE LEVĘL LEVEL LEVEL (20AK 21A2 20A1 4BK **4B3** 4B2 DELAY FIRST STAGE OFLAY OBCUIT DELAY ΑF MF ¥ 4AK 4A2 4A3 $20A2^{3}$ INCOMING SIGNAL T

OBLON, SPIVAK, et al

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OBLON, SPIVALOR tal Docket No: 1967 12-2 Inventor: Takeo SoZUKI, et al. Serial No: 09/650,084 Reply to OA dated: February 25, 2004 Replacement Sheet





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